Lab #5 The OR Gate

Name:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Using the schematic below hook up the circuit using any one of the OR gates on the IC. Remember that you must power the chip in order for it to work. Have your teacher sign the circuit indicating the circuit works.



Verify and complete the truth table.

Instructor Signature:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Truth Table

|  |  |  |
| --- | --- | --- |
| A | B | Y |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |